

START S 200

START S 200

202

Providing selectable Passband ranges

Selecting Passband - 204

206

determining clock frequency that

Produces no substantial spurs

Adjusting clock = 208

210

driving processor with clock signal

Fig. 2

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t-JA1-3 Initially generating 19,2 mHz clock frequency with a harmonic at 883,2MHz generating clock frequency with harmonics 1st frequency generating transceiver carrier at 2nd frequency Setecting clock trequency so that 1st frequency does not equal 2nd frequency

F19.3

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•
Fg. 4 START 5400
generating clock harmonics at 1st frequency y
frequency 434
francisco de la companya del companya de la companya del companya de la companya del la companya de la companya
generating transceiver at 2nd frequency
changing clock frequency so that ist frequency doesn't equal 2nd frequency
changing clock frequency so that ist
frequency doesn't equal 2nd trequency
And is the contract of the second of the sec
STAP2+ 500 502
070 371 463
generating clock harmonics at 1st frequency 504
504
Trenerating transceiver at 2nd frequency moth
generating transceiver at 2nd frequency mother
generating transceiver at 2nd frequency motife equat to the 1st frequency 506
changing transceiver to lit trequency
changing transceiver to lit frequency 598
changing transceiver to let frequency 508 Changing clock harmonics to 3rd frequency
changing transceiver to let frequency 508 Changing clock harmonics to 3rd frequency
changing transceiver to lit frequency 598

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